

## **ABSTRACT**

A flash memory broadcasting displayer panel is comprised of 1) a flash memory card, 2) an interface card, and 3) a Liquid Crystal Displayer. Time  
5 sequence type files, which are stored in the flash memory card, are processed by the interface card and transferred to the Liquid Crystal Displayer. The interface card includes a microprocessor operating with an internal BUS to control, process and route signals between the flash memory and the Liquid Crystal Displayer.

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